INDRAPRASTHA INSTITUTE OF INFORMATION TECHNOLOGY DELHI

(A State University established by Govt. of NCT of Delhi)

Date: 15/09/2021

Subject: Applications are invited for Research Engineer (RE) positions at Algorithms to Architecture Lab, ECE Department, IIIT Delhi

Closing date of Application- 15/10/2021

Post	TWO Positions: 1) RE in 6G: Machine Learning Algorithms for Physical Layer, 2) RE in VLSI: Reconfigurable and Intelligent Architectures for AI. **For more information: www.iiitd.edu.in/~sumit/**
Monthly Remunerations	**Rs. 31,000/- (+ HRA)**
Essential Qualifications	• Master degree in ECE with good academic record (CGPA >= 7.5) • Project/thesis work topics related to wireless communication, machine learning or FPGAs • Good interpersonal and communication skills (both oral and written)
Desirable Qualifications and Experience Duration	• Proficient in Matlab and Python or Proficient in Verilog, C/C++ and Python • Publications at good venues
Duration	2 years
Application Form	• Please submit your application here: https://forms.gle/KsAahYjRZdZC7SaD9 • Please submit your application on or before Aug. 15, 2021. The positions will be filled as soon as a suitable candidate is found.

Regards,
Sumit J Darak
Associate Professor,
Contact No. : 011-26907427
http://faculty.iiitd.ac.in/~sumit/